

IN THE CLAIMS:

Please cancel claims 1-8 of the prior application.

Please add the following new claims:

12. (new) The invention as defined in claim 9 wherein said circuitry includes a pad on each of said filled plated through holes.

13. (new) The invention as defined in claim 10 wherein said circuitry includes a pad on each of said filled plated through holes.

14. (new) The invention as defined in claim 11 wherein said circuitry includes a pad on each of said filled plated through holes.

15. (new) The invention as defined in claim 9 further characterized by a layer of dielectric material disposed on said dielectric substrate and overlying said circuitry on said dielectric substrate, said layer of dielectric material having at least one via formed therein.

16. (new) The invention as defined in claim 10 further characterized by a layer of dielectric material disposed on said dielectric substrate and overlying said circuitry on said dielectric substrate, said layer of dielectric material having at least one via formed therein.

17. (new) The invention as defined in claim 11 further characterized by a layer of dielectric material disposed on said dielectric substrate and overlying said circuitry on said dielectric substrate, said layer of dielectric material having at least one via formed therein.

18. (new) The invention as defined in claim 15 further characterized by circuitry disposed on said layer of dielectric material.

19. (new) The invention as defined in claim 16 further characterized by circuitry disposed on said layer of dielectric material.

20. (new) The invention as defined in claim 17 further characterized by circuitry disposed on said layer of dielectric material.

REMARKS

The new claims do not constitute new matter since they find support in the specification on page 10, line 29, through page 11, line 30.

Respectfully submitted,

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